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07/798,869 11/25/91 NILSSEN

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EXAMINER

SHINGLETON, M

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BARRINGTON, IL 60010ART UNIT
2502PAPER NUMBER
8

DATE MAILED: 01/31/92

This is a communication from the examiner in charge of your application.
COMMISSIONER OF PATENTS AND TRADEMARKS This application has been examined Responsive to communication filed on 11-25-91 This action is made final.A shortened statutory period for response to this action is set to expire 3 month(s), 0 days from the date of this letter.
Failure to respond within the period for response will cause the application to become abandoned. 35 U.S.C. 133

Part I THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

1. Notice of References Cited by Examiner, PTO-892.
2. Notice re Patent Drawing, PTO-948.
3. Notice of Art Cited by Applicant, PTO-1449.
4. Notice of Informal Patent Application, Form PTO-152.
5. Information on How to Effect Drawing Changes, PTO-1474.
6.

Part II SUMMARY OF ACTION

1. Claims 25-40 are pending in the application.
Of the above, claims _____ are withdrawn from consideration.
2. Claims 1-24 have been cancelled.
3. Claims _____ are allowed.
4. Claims 25-40 are rejected.
5. Claims _____ are objected to.
6. Claims _____ are subject to restriction or election requirement.
7. This application has been filed with informal drawings under 37 C.F.R. 1.85 which are acceptable for examination purposes.
8. Formal drawings are required in response to this Office action.
9. The corrected or substitute drawings have been received on _____. Under 37 C.F.R. 1.84 these drawings are acceptable. not acceptable (see explanation or Notice re Patent Drawing, PTO-948).
10. The proposed additional or substitute sheet(s) of drawings, filed on _____ has (have) been approved by the examiner. disapproved by the examiner (see explanation).
11. The proposed drawing correction, filed on _____, has been approved. disapproved (see explanation).
12. Acknowledgment is made of the claim for priority under U.S.C. 119. The certified copy has been received not been received been filed in parent application, serial no. _____; filed on _____.
13. Since this application appears to be in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.
14. Other

EXAMINER'S ACTION

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The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

The specification is objected to under 35 U.S.C. 112, first paragraph, as the specification, as originally filed, does not provide support for the invention as is now claimed.

What is now claimed is specific ranges like "the second brief span of time is at least 10% longer than the duration of each half-period" and "the first brief span of time is shorter than 90% of the duration of each half-period". However, the original disclosure is not so limited. On page 1 and the paragraph bridging pages 7 and 8, the original disclosure sets forth that "[t]he ON-time (or forward conduction period) of each of the inverter's two transistors is shorter than half the period of the high frequency voltage...". The original disclosure also makes reference to Figure 3. Both of these disclosures fail to recite or even suggest the specific ranges as is now claimed.

Claims 26,27,29,31,35,39, are rejected under 35 U.S.C. 112, first paragraph, for the reasons set forth in the objection to the specification.

Claims 25-40 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly

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point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 25 is indefinite for the limitations setting forth that "each half period being of substantially equal duration" just can't be correct. The first and second half-periods make up the fundamental period and the fact that two halves make a whole is very well known. By definition a half is equal to $1/2$ of the total. Thus, the two halves must be of equal duration with respect to each other, otherwise they just can't have the name of "half". The use of this in any other way would be repugnant to the usual meaning of the word. The two halves just can't be of substantially equal duration for this would go against the meaning of the term half.

Claim 25 is also functional in nature. The functional language beginning with line 26 just does not have sufficient structure set forth in the claim so as to warrant the presence of the functional language in the claim. The claim does set forth a first transistor and that it conducts "current" in response to a control voltage that is provided at a control input. However, no means is set forth for supplying that control voltage and thus no means is set forth that can enable the function of causing the first transistor to be "operative" to allow current to flow, i.e. the transistor becomes conductive.

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Claim 34 is unclear for it first recites that the high frequency output current has a fundamental period and then refers to "each fundamental period". Is there more than one fundamental period? The claims also recite a limitation that is similar to a limitation in claim 25 and that is "each half-period being substantially of equal duration". If two half periods make a whole, they can not be substantially equal to a half-period they must be equal to the half-period. Two halves makes a whole and not substantially a whole, for substantially a whole would might still fall short even by a small insignificant amount and this still would not be the whole. In other words a half is equal to 1/2 times the value of the whole by definition, not substantially equal to 1/2 times the value of the whole.

Claim 34 is indefinite for it does not positively recite the existance of means for the supplying of the recited first and second control voltages supplied to the first and second control inputs. In other words the functional language of providing these voltages is not supported by sufficient structure to warrant the presence of this in the claim.

Claim 28 is indefinite for similar reasons claim 34 is indefinite involving functional language.

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Claim 40 is also indefinite for it does not positively recite that the control signal is indeed supplied to the transistor. It only recites that the transistor is such that it can receive a control signal. The functional language of lines 14-20 is unsupported by the structure set forth in the claim. This is so in much the same way claim 25 is indefinite due to similar functional problems. First, the claim does not positively recite that a control signal is indeed supplied and second, no means is set forth that provides the necessary recitation of structure that provides for this control signal. How can the control signal exceed a valve if in fact there is no means that would enable this signal to exceed this value or even to produce this control signal at all? The claim should properly recite a means for supplying a control signal in which such means includes further means that enables the control signal to have a variable voltage level or voltage magnitude.

Claims whose base claims indicated in the text of this rejection incorporate the indefiniteness of these base claims and are therefore themselves indefinite.

The following is a quotation of 35 U.S.C. 103 which forms the basis for all obviousness rejections set forth in this Office action:

A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between

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the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Subject matter developed by another person, which qualifies as prior art only under subsection (f) or (g) of section 102 of this title, shall not preclude patentability under this section where the subject matter and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person.

Claims 25,28,30,32,33,34,36,37,38,40 are rejected under 35 U.S.C. 103 as being unpatentable over Dale et al. in view of Wotowiec of record and Stolz or Zansky.

Dale et al. of record discloses all aspects of the invention as set forth by the above claims, except for the use of the screw-in inverter housing itself to support the gas discharge lamp and is silent on the fact that the specific semiconductor inverter arrangement involves the switching of the transistor(s) on, that compose the inverter, for a period that is "substantially shorter than the duration of each half-period".

Wotowiec discloses a screw-in ballast arrangement also using a "circline" type lamp in which the housing of the ballast actually forms the supporting structure for the lamp itself. One clear advantage of the structure of Wotowiec is the integration into one easy to change unit the inverter and lamp. Another is the inherent safety aspects of such a design. This is so because the isolation of the output terminals of the lamp is possible.

It would have been obvious to a person having ordinary

skill in the art at the time the invention was made to integrate into one unit the "circline" lamp and the screw-in ballast housing so as to obtain a structure that "seals" the lamp terminals and thereby reducing the risk of shock to a person coming in contact with the lamp fixture, among many other reasons clearly taught by Wotowiec.

Both Zansky and Stolz disclose well known semiconductor inverter ballast arrangements for the powering and the dimming of gas discharge lamps. These circuits are very similar to the semiconductor inverter ballast of Dale et al. and are truly functionally equivalent to that of Dale et al.. The circuits of both Zansky and Stolz specifically recite the feature of the instant invention which includes the control voltage applied to one or more of the switching transistor(s) that allows this/these switching transistor(s) to conduct current for a brief span of time that is substantially shorter than that of a half-period.

Thus, given that the inverter circuits of both Zansky and Stolz are functionally equivalent to that of Dale et al. the incorporation of such inverter circuits in Dale et al. would have been obvious to a person having ordinary skill in the art at the time the invention was made. This truly results in the placement of a specific inverter ballast in a housing arrangement that is already well known for the housing of inverter ballasts of this type. Also the selection of the inverter ballast used just happens to be one dictated by the situation one desires. If efficiency is desired one of ordinary skill would have found it obvious to use a inverter ballast whose efficiency is high. The same is true if one is concerned about power factor, cost, etc.. As one can clearly see numerous inverter ballasts would have been obvious replacements to that in Dale et al..

Claims 25-40 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-21 of U.S. Patent No. 4,857,806. Although the conflicting claims are not identical, they are not patentably distinct from each other because the claims only differ by a matter of minor wording. Stated in another way all the limitations presented is also set forth in the claims of U.S. Patent 4,857,806, but said in a slightly different way. For example claim 25 of the instant application sets forth that the brief span of time the current is permitted to flow is shorter than the duration of a half period. This is presented in claim 17 of U.S. Patent 4,857,806 as being shorter than one quarter of the cycle period. Clearly, one quarter is shorter than half the period. It is noted that claims of the U.S. Patent 4,857,806 includes items that are not recited in the instant claims of the instant application and thus the issue of domination applies.

The obviousness-type double patenting rejection is a judicially established doctrine based upon public policy and is primarily intended to prevent prolongation of the patent term by prohibiting claims in a second patent not patentably distinct from claims in a first patent. In re Voges, 164 USPQ 619 (CCPA 1970). A timely filed terminal disclaimer in compliance with 37 C.F.R. 1.321(b) would overcome an actual or provisional rejection on this ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 C.F.R. 1.78(d).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Shingleton whose telephone number is (703) 308-4903. Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0956.

mbs
Shingleton/mbs
January 23, 1992

Eugene R. Laroche
EUGENE R. LAROCHE
SUPERVISORY PATENT EXAMINER
GROUP ART UNIT 252